

BUILDING

A MICROELECTRONICS INFRASTRUCTURE

The Microelectronics Lab of the Purdue Applied Research Institute (PARI) reinforces this critical science as the linchpin of national security, cutting across important areas from weapons systems to infrastructure.

Microelectronics technology spans critical areas, including weapons, information technology, sensing and warning systems, control of critical infrastructure, space applications and many more. With cross-functional teams, the PARI Microelectronics Lab (PML) drives innovation, solves complex technical challenges and develops state-of-the-art technologies to meet vital Defense Department needs. Our research and development efforts span design, modeling, simulation, fabrication, characterization and packaging of novel microelectronic technologies and devices.

PRIMARY FOCUS AREAS

Microelectronics in extreme environments

Developing chips and packaging for applications such as hypersonics and space that experience extremes in radiation, temperature, shock and vibration

Materials and processes

Optimizing processes for microelectronics fabrications and facilities to best meet national security needs

Modeling and simulation

Building validated software to streamline decisions and designs and accelerate technology development

Silicon photonics

Leveraging silicon photonics to develop integrated sensing, biomedical, automotive, aerospace, quantum and artificial intelligence applications

LOCATIONS IN

ODON,
INDIANA

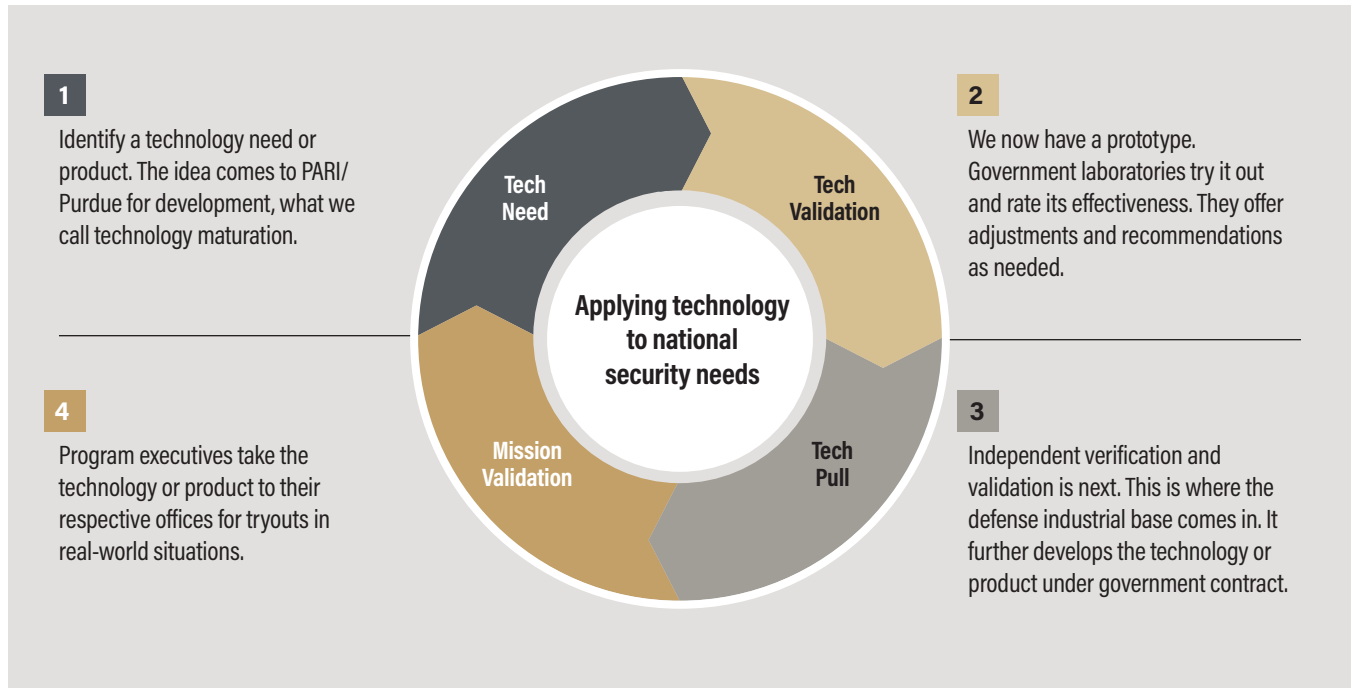
AND

WEST
LAFAYETTE,
INDIANA



LEARN
MORE

TRANSFORMING IDEAS INTO REALITY



CAPABILITIES

Subject matter experts

PML staff researchers and the breadth of Purdue University faculty and staff to augment and consult with your team.

Access to world-class facilities

Classified laboratories, government and industry partner labs, and one of the largest academic cleanrooms in the country with complete microelectronic and packaging processing

Workforce development

Building a pipeline from academia to industry to fill specialized jobs in microelectronics

Research and development

Developing new designs through to modeling and simulation to create prototypes and test vehicles

PML'S KEY ADVANTAGES

- New lab facilities in renovated and accredited space for classified U.S. government work
- Alliances and partnerships with industry and the DoD to conduct applied research in targeted areas and build relevant microelectronics capabilities
- Partnership with Naval Surface Warfare Center, Crane Division for broad experimentation and resources
- Workforce development via training courses, web-based applications and internships
- Our experience and reputation for collaboration enable us to work seamlessly across government, industry and academia.



Dr. Darren Crum, Director
 djcrum@purdue.edu
 203 S. Martin Jischke Dr.
 West Lafayette, IN 47907